

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:  
MCGUINNESS, PATRICK J.

Patent No.: 7124385 B2

Application No.: 10/657609

Issue Date: 4/4/2006

Filed: 09-08-2003

Examiner: Vuthe Siek

Title: METHOD FOR AUTOMATED TRANSISTOR FOLDING

**Certificate of Submission**

I hereby certify that this correspondence is being submitted to the USPTO, Alexandria, VA.

- Addressed per C.F.R. § 1.1(a) and deposited with the United States Postal Service with sufficient postage as first class mail.
- Facsimile transmitted in accordance with C.F.R. §1.6(d).
- Submitted electronically via EFS in accordance with "Legal Framework for EFS Web".

May 30, 2008

Date of Submission

Teresa Shephard

Signature

Teresa Shephard

Printed Name of Person Signing Certificate

Commissioner for Patents  
Alexandria, VA 22313

**SUBMISSION OF CERTIFICATE OF CORRECTION**

Dear Commissioner:

Enclosed is a Certificate of Correction listing error(s) in the subject patent. Please enter these corrections. Since the error(s) appear to be on the part of the Applicant, the required fee will be charged to Deposit Account 503079 via the Electronic Filing System.

Shn/RJ  
Date

Respectfully submitted,

Ranjeev Singh

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**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

Page 1 of 1

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FIRST NAMED INVENTOR: MCGUINNESS, PATRICK J.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In Column 20, Line 18, Claim No. 3:

Change "create modified fold solution from" to--creating modified fold solution  
from--

In Column 22, Line 59, Claim No. 17:

Change "create modified fold solution from" to--creating modified fold solution  
from--

MAILING ADDRESS OF SENDER (Please do not use customer number below)

Freescale Semiconductor, Inc.

Law Department

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